

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Appl. No. : 10/560,573 Confirmation No. 6633  
First Inventor : Theodore LETAVIC  
Filed : December 12, 2005  
TC/A.U. : 2523  
Examiner : Brook Kebede

Docket No. : **US03 0162 US2**  
Customer No. : 65913

Title: High-Side CMOS In An Integrated SOI Process

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**RESPONSE & AMENDMENT**

Sir:

In response to the Office Action of February 14, 2007, please consider the following. Note that an SB-122 Change of Correspondence Address is included in these papers.

**Amendments to the Specification** there are no amendments in this paper.

**Amendments to the Claims** begin on page 2 of this paper.

**Remarks/Arguments** begin on page 3 of this paper.